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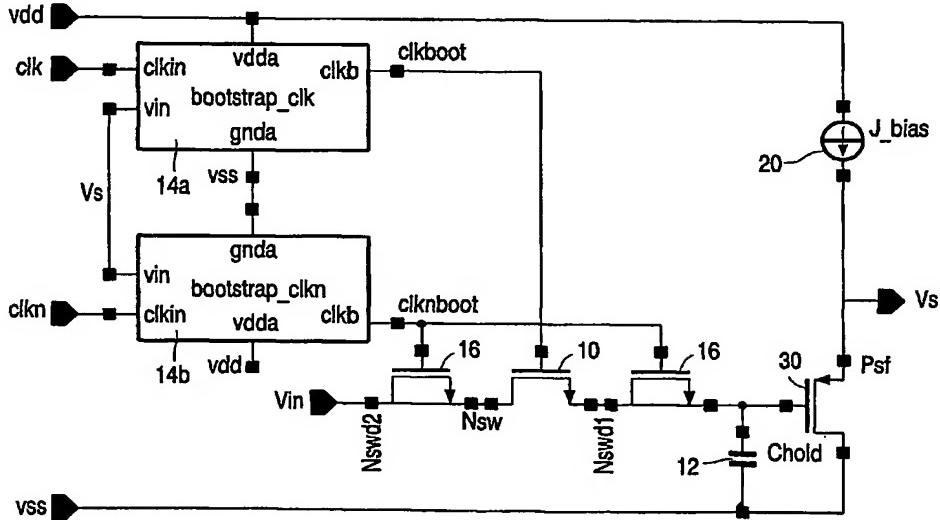
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(54) Title: TRACK-AND-HOLD CIRCUIT



(57) Abstract: A track-and-hold circuit comprising a switch (10) and a capacitor (12). A first bootstrap switch (14a) has as its inputs a clock signal clkin and an input signal Vin. The clock signal clkboot output from the first bootstrap switch (14a) is applied to the gate of the switch (10). The first bootstrap switch (14a) is connected between the input Vin and the output Vs of the circuit via level shifting means, in the form of a current source (20), and buffer means (30). A second bootstrap switch (14b) is provided, having as its inputs the clock signal clkin and the input signal Vin. The anti-phase clock signal clkboot output from the second bootstrap switch (14b) is applied to the gates of two dummy switches (16) connected on either side of the switch (10).

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